

ABSTRACT OF THE DISCLOSURE

In the memory management method, when a failure is detected in the power supplied to a protective memory, a control toward the memory is cut off from a CPU and the power supply is switched to a standby power before the protective memory has some damaging effect; after solution of the power failure, the switching is finished, the control toward the memory is returned to the CPU, and the power supply is returned to an ordinal one; write completion of data is notified to the client at a time when the data sent from the client has been written into the protective memory; whether the last termination is abnormal or normal is checked at the activation; when the last termination is abnormal, the protective memory is returned after memories other than the protective memory are initialized at the activation; and when the last termination is normal, the protective memory is returned and thereafter all the memories are initialized at the activation.

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